EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	366	(712/208).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:23
S2	144	(712/211).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:23
S3	319	(712/233).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:24
S4	397	(712/234).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:24
S5	529	(712/244).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:24
S6	1	(compar\$ near4 branch\$3) with (array near4 (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:22
S7	23	(compar\$ near4 branch\$3) same (array near4 (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:29
S8	1	("6332215").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:29
S9	4	(compar\$ with branch\$3) with (array with (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:35
S10	139	(exception\$1) with (array with (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:35
S11	3	(exception\$1) with (array with (boundar\$3 or end\$4 or empt\$5 or full\$4)) with branch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 11:39

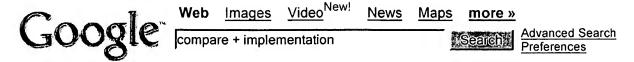
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S12	23	(exception\$1) near4 (array near4 (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:36
S13	11	(compar\$ near4 branch\$3) with exception\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:34
S14		(compar\$ near4 (branch\$3 or jump\$3)) same ((exception\$1 or interrupt\$1) near4 array\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:35
S15	36	(exception\$1 or interrupt\$1) near4 (array near4 (boundar\$3 or end\$4 or empt\$5 or full\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:36
S16	291	(exception\$1 or interrupt\$1) near4 (branch\$3 or jump\$3) near4 (compare or conditional)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:54
S17	0	(exception\$1 or interrupt\$1) near4 (branch\$3 or jump\$3) near4 (compare or conditional) near4 (pre?determin\$3 or pre?program\$4 or know\$3) near4 (target near4 address\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:55
S18	0	(exception\$1 or interrupt\$1) near4 (branch\$3 or jump\$3) near4 (compare or conditional) near4(target near4 address\$2)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:55
S19	9	(branch\$3 or jump\$3) near4 (compare or conditional) near4 (exception\$1 or interrupt\$1) near4 (handler\$3 or rountine\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/03 12:56
S20	376	(712/208).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/18 12:55
S21	146	(712/211).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/18 12:55
S22	331	(712/233).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/18 12:55

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S23	406	(712/234).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/18 12:55
S24	548	(712/244).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/18 12:55
S25	41	(("3889243") or ("4236204") or ("4587632") or ("4969091") or ("4922414") or ("5136696") or ("5455775") or ("5619665") or ("5638525") or ("5659703") or ("5740461") or ("5742802") or ("5752035") or ("5784584") or ("5809336") or ("5892966") or ("5875336") or ("5892966") or ("5925123") or ("5926832") or ("5937193") or ("593741") or ("6003126") or ("6009499") or ("6009509") or ("6014723") or ("6021469") or ("6026485") or ("6070173") or ("6038643") or ("6122638") or ("6125439") or ("6148391") or ("6298434") or ("6317872") or ("6338134") or ("6349377") or ("6374286") or ("6606743")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/09/21 15:48

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Results 1 - 10 of about 53,000,000 for compare + implementation. (0.23 seconds)

<u>Digital Mars - digitalmars.D.bugs - incorrect compare ...</u> digitalmars.D.bugs - incorrect compare implementation in ti_Aulong and ti_Along ... assert(ti.compare(a,b)>0); return 0; ... www.digitalmars.com/d/archives/digitalmars/D/bugs/5081.html - 8k - Cached - Similar pages

incorrect compare implementation in ti Aulong and ti Along ... incorrect compare implementation in ti_Aulong and ti_Along, Digital Mars. Thomas_Kühne (), 2005/09/28 04:14 ... assert(ti.compare(a,b)>0); return 0; ... www.digitalmars.com/pnews/read.php?server=news. digitalmars.com&group=digitalmars.D.bugs&artnum=5081 - 7k - Cached - Similar pages

Post-Implementation Review

A Post-Implementation Review (PIR) is an assessment and review of the completed working ... How does that **compare** to the original project definition? ... www.epmbook.com/pir.htm - 11k - <u>Cached</u> - <u>Similar pages</u>

[PDF] Citizen Review of Police: Approaches and Implementation ((Issues ... File Format: PDF/Adobe Acrobat and Implementation is designed to help jurisdictions that ... difficult to compare the effectiveness of different citizen review models because they have ... www.ncjrs.gov/pdffiles1/nij/184430.pdf - Similar pages

A Standards-based XML Schema Implementation Comparison Framework
A Standards-based XML Schema Implementation Comparison Framework. Henry
S. Thompson [HCRC Language Technology Group; World Wide Web Consortium] ...
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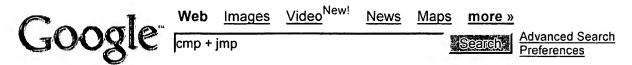
ICANN | ICANN Accra Meeting Topic: Implementation of Independent ... ICANN Accra Meeting Topic: Implementation of Independent Review Policy ... the scope of independent review under the current policy is "to compare contested ... www.icann.org/accra/indrev-topic.htm - 16k - Cached - Similar pages

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Results 1 - 10 of about 461,000 for cmp + jmp. (0.47 seconds)

devik - Re: Porting gcc for F-CPU without direct JMP

Like: a: add ... cmp ... jmp a will be replaced a: add .. cmp .. mov a,r1 jmp [r1] and later optimized: loadaddr r1; get current PC a: add .. cmp .. jmp ... gcc.gnu.org/ml/gcc/2002-12/msg00581.html - 6k - Cached - Similar pages

Please Help - Critical Security.NET

There's also some hints about reading up on some Assembly functions (MOV, PUSH, POP, **CMP**, **JMP**), which are worth hitting Google to find some info on, ... www.criticalsecurity.net/index.php?showtopic=12379&view=getlastpost - 48k - Cached - Similar pages

;sines.asm; ;douglas irving repetto april.97;glmrboy@shoko ...
cmp a,b jeq D_ONE move #\$40,b1; ascii '@' - this is for a mac keyboard! ...; ascii 'm'
cmp a,b jeq MODE_MIX move #\$4D,b1; ascii 'M' cmp a,b jeq MODE_MOD jmp ...
www.musicdsp.org/files/sines.asm - 9k - Cached - Similar pages

BF interpreter for Menuet by VT; Compile with FASM 1.60 or ...
... inc r13 cmp r13, 10000 jb noeventcheck mov r13, 0 mov rax, 11 int 0x60 cmp rax, 0 je noevents test rax, 4 jz nobutton mov rax, 0x11 int 0x60 jmp ...
www.menuetos.net/bf.asm - 13k - Cached - Similar pages

info: Chronos, Bryant Brownell, Oregon State University; General ... (jmp not actually needed) bump: cmp r5, 80 //allow a certain number of bumps, then stop trying to repair/clone jg continueBump //this is to speed up the ... www.symantec.com/specprog/university/bryant_brownell.asm - 10k - Cached - Similar pages

;Prototype driver for IDE/ATA interface board ;65C02 assembly ...
... like CD) or nothing cmp secnr bne signok lda #\$0 cmp cyllow bne signok cmp ...
lda secbuf+\$1FE cmp #\$55 beq]55ok jmp]signok]55ok lda secbuf+\$1FF cmp ...
s.guillard.free.fr/Apple2IDE/lwlv-src.txt - 11k - Cached - Similar pages

,]P^0Dw0Dy5{!P/5e PZX5'(n!`This is the source code, not the ... JNZ O6 MOV AH,02h MOV DL,[M+BX] INT 21h **JMP** DO ;IF O\$ = "," THEN M(P) = ASC(INPUT\$(1)) O6: **CMP** AL,"," ; , JNZ O7 MOV AH,08h INT 21h MOV [M+BX],AL **JMP** DO ;IF ...

esoteric.sange.fi/brainfuck/impl/interp/BFI.ASM - 6k - Cached - Similar pages

MACHFELD|visp

CMP #\$00 BEQ LOD1 JSR CLONE LOD1 JSR \$A316 JMP \$A416 BLOD JSR TESTON LDA FLAG1 ... DEST16 CMP #78 BNE DEST17 JMP \$C600 DEST17 CMP #79 BNE DEST18 JSR READ ...

www.machfeld.net/visp/source.html - 12k - Cached - Similar pages

shadow.asm By Michael Kohn; October 5,1995 .model tiny .code ...